

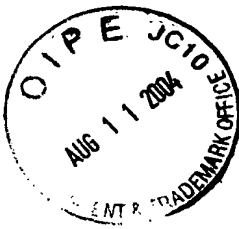
VERIFICATION OF TRANSLATION

I, the below-named person, hereby certify that I am familiar with both the Japanese and the English language, that I have reviewed the attached English translation of U.S. Patent Application Serial No. 10/816,837, filed April 5, 2004, and that the English translation is an accurate translation of the corresponding Japanese language paper.

I further declare that all statements made in this declaration of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful, false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of legal decisions of any nature based on them.

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Date

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COLOR IMAGE FORMING APPARATUS

BACKGROUND OF THE INVENTION

1. Field of the Invention

5 The present invention relates to a color image forming apparatus which forms a color image, in particular, to an improved processing control technique of image data.

2. Description of Related Art

10 In the field of image forming apparatus, such as a color copy machine, color page printer, etc., which performs color printing by one page through the electronic photographic method, as one of the color printing methods, the four-rotation method is known.

15 According to the four-rotation method, one printing unit, which consists of a photosensitive member, a toner drum, etc., is provided, whereby element colors such as, yellow (Y), magenta (M), cyan (C), and black (B) are sequentially transferred on a intermediate transfer

20 member until transfer with respect to all the element colors is completed; and after completing the transfer on the intermediate transfer member, the image carried on the intermediate transfer member is transferred on a piece of paper, whereby color printing is performed.

25 Under this method, transfer of each element color onto the intermediate transfer unit is performed in a manner that the transferring operations do not overlap one another in terms of timing as shown in Fig. 4, that is,

after completing transfer of an element color, the transfer of the next element color is started.

As an image processing control mechanism which realizes the printing according to the above-mentioned
5 four rotation method, the mechanism shown in Fig. 5 conventionally is known. According to this type, with respect to one element color, one pair of a RAM/register unit and image processing unit is provided (that is, four pairs are provided in the entire apparatus),
10 transfer of each element color is performed by the pair associated with the element color. However, due to the configuration in which one picture image processing unit is provided with respect to each element color; when an element color is transferred, the other image processing
15 units associated with the other element colors are not used. Thus, this type is problematic in that there is a redundancy in the constitution, so that a useless production cost is generated.

As an image processing mechanism of the other types
20 which solves the redundancy of the constitution, the image processing mechanism shown in fig. 6 is known. According to this type, only one pair of RAM/register unit and a image processing unit is provided, and changing of element color to be transferred is executed
25 by rewriting the stored contents in the RAM/register unit. However, this type is problematic in that the rewriting requires a predetermined time, so that the printing speed is slowed down, and then the apparatus

cannot cope with the recent tendency that high-speed printing is demanded. In particular, this problem has become noticeable these days because there is a tendency that the color images are becoming of high-quality which
5 increases the data amount required for image processing and consequently elongating the rewriting time.

SUMMARY OF THE INVENTION

Accordingly, an object of the present invention is
10 to provide a color image forming apparatus which enables high-speed printing without complicating the constitution of the image processing control mechanism.

The present invention provides a color image forming apparatus (for example, color copy machine) in
15 which images of a plurality of element colors (for example, yellow, magenta, cyan, black) are sequentially transferred to form a color image, the color image forming apparatus comprising: (i) control means (for example, CPU) for giving control data, to be used for
20 processing control of the image data associated with the element color, and address; (ii) a plurality of temporary storage means (for example, RAM/register unit) each being provided for each of the element colors and temporarily storing control data supplied from the
25 control means in an address of its own obtained on the basis of the address supplied from the control means; (iii) writing selection means (for example, chip select signal generating unit) which generates, on the basis of

the address supplied from the control means, the selection signal to select one of the plurality of temporary storage means; (iv) image processing means (for example, image processing unit) which processes, on
5 the basis of the control data stored in the plurality of temporary storage means, an image data associated with a received element color; and (v) printing means (for example, printer unit) which performs transfer of an element color on the basis of the processing result of
10 the image processing means and gives a color code representing an element color which is being subjected to transferring.

Accordingly, it becomes possible to perform image processing adequate for each element colors to be
15 printed, by storing control data with respect to a plurality of element colors in the temporary storage means previously and switching the corresponding temporary storage means.

20 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view showing the constitution of an image forming processing control unit in the color image forming apparatus according to a first embodiment of the present invention;

25 Fig. 2 is a table which shows the relationship between an address space and respective element color RAM/register units and the like;

Fig. 3 is a view showing the constitution of an

image processing control unit in the color image forming apparatus according to a second embodiment of the present invention;

Fig. 4 is a chart schematically showing the timing of the transfer of each element color according to a four-rotation method;

Fig. 5 is a view showing an example of the constitution of a conventional image processing control unit; and

Fig. 6 is a view showing another example of a conventional image processing control unit.

BEST MODE FOR CARRYING OUT THE INVENTION

(1) First Embodiment

Hereinafter, referring to Figs. 1 and 2, a first embodiment of the image forming apparatus according to the present invention will be described.

(1-1) Constitution of First Embodiment

As shown in Fig. 1, the color image forming apparatus according to the first embodiment is roughly constituted by a CPU (central processing unit) 11, chip select signal generating unit 12, respective element color RAM/register units 13-1 to 13-4, an image processing unit 14, and a printer unit 15.

Here, the CPU 11 is a unit which is constituted by, for example, a micro computer, and adapted to supply control data to the respective RAM/register units 13-1 to 13-4, and to supply an absolute address to the chip

select signal generating unit 12 and the respective RAM/register units 13-1 to 13-4.

5 The chip select signal generating unit 12 is a unit which is adapted to decode the absolute address supplied from the CPU 11, and to generate a chip select signal on the basis of the data obtained by the decoding, and supply the respective RAM/register units 13-1 to 13-4.

10 The respective element color RAM/register units 13-1 to 13-4 are units each of which is adapted to receive the control address and the absolute address from the CPU 11, and a chip select signal from the chip select signal generating unit 12, and to give control data to the image processing unit 14. The element color RAM/register units are provided with respect to four
15 colors: yellow (hereinafter occasionally referred to as "Y" or "Y-color"), magenta (hereinafter occasionally referred to as "M" or "M-color"), cyan (hereinafter occasionally referred to as "C" or "C-color"), and black (hereinafter occasionally referred to as "B" or "B-
20 color") (hereinafter, these four colors are occasionally referred to as "element color"). The number of RAMs and registers of each element color RAM/register unit 13-1 ~ 13-4 are arbitrary. The respective element color RAM/register units 13-1 to 13-4 mutually have the same
25 configuration. Accordingly, the reason why the respective element color RAM/register unit executes their specific actions, that is, actions different those of the other element color RAM/register units is that

the information stored in the respective element color RAM/register units are different from one another. The reference numerals employed for the respective element color RAM/register units are: 13-1 for Y-color RAM/register unit; 13-2 for M-color RAM/register unit; 13-3 for C-color RAM/register unit; and 13-4 for K-color RAM/register unit.

The image processing unit 14 is a unit which is adapted to process a color image data read by and transferred from an image reading unit (not shown), on the basis of the control data supplied from the respective RAM/register units 13-1 to 13-4, and to supply the image data thus processed to the printer unit 15. The image processing unit 14 also receives a color code supplied from the printer unit 15.

The printer unit 15 is a unit which performs printing action on the basis of processed image data supplied from the image processing unit 14. The printer unit 15 also supplies a color code to the image processing unit 14.

(1-2) Operation of first embodiment

Next, referring to Figs. 1 and 2, the operations of the color image processing apparatus having the above-mentioned constitution will be described. The operations are different depending on the cases: where the CPU accesses the respective element color RAM/register unit and when a printing action is performed, so that explanations will be made with respect to these cases.

(1-2-1) In accessing from CPU

The operations performed when accessed from the CPU will be described. In the following explanations will be made on an assumption that the address space associated with the CPU output and the RAM/register units of the respective element colors have the relationship shown in Fig. 2. Further, an explanation will be made by way of the case where a control data FFh is written in the absolute address 1008h.

First, an absolute address 1008h is supplied from the CPU 11 to the chip select signal generating unit 12. At the chip select signal generating unit 12, the absolute address 1008h thus supplied is decoded, so that a chip select signal is generated on the basis of the data obtained through the decoding. In this case, the uppermost digit of the address supplied from the CPU 11 is "1", the chip select signal to be generated is, as shown in Fig. 2, MCS-0, which causes only the M-color RAM/register unit to be write-enable state. Then, the chip select signal MCS-0 thus generated is supplied to the respective element color RAM/register units 13-1 to 13-4. In the present embodiment, the chip select signal is defined as negative logic (active low). Specifically, a signal of an L-level serves as a chip select signal, and the writing-in of data is performed with respect to the RAM/register unit which has received such a chip select signal.

In parallel with this, at the CPU 11, "008h" of the

lowermost three digits of the absolute address 1008h is separated. Then, the control data FFh corresponding to the address, along with the lowermost three-digit addresses "008h" obtained by the separation, is supplied to each of the element color RAM/register units 13-1 to 13-4.

At each of the element color RAM/register units 13-1 to 13-4, writing or non-writing, of the data, is executed on the basis of the signals and data, thus supplied. Specifically, in this case, since the chip select signal MCS-0 thus supplied is a signal which causes only the M-color RAM/register unit 13-2 to be a write-enable state, and an address 008h in the RAM/register unit 13-2 is specified, the control data is written in the address 008h of the RAM in the RAM/register unit 13-2. By contrast, the remaining Y-color RAM/register unit 13-1, C-color RAM/register unit 13-3, and K-color RAM/register unit 13-4 are not caused to be a write-enable state by the chip select signal MCS-0, the above-mentioned writing is not executed at the Y, C and K-color RAM/register units 13-1, 13-3, 13-4.

In the case where an address and a control data are other than the above-mentioned ones, the same processings are performed. For example, the absolute address output from the CPU 11 is 2006h and the control data is E7h, a chip select signal CCS-0 is generated, and the control data E7h is written in 006h of the C-color RAM/register unit 13-3.

In this way, during the period when the CPU 11 is accessed, when time is comparatively ample, the absolute address and control data are sequentially output, and written in the respective RAM/register units by way of the above procedure. This writing operations are continued till supplying of the control data and address from the CPU 11 to the chip select signal generating unit 12 and RAM/register units 13-1 to 13-4 are terminated.

10 (1-2-2) Print-outputting

When the writing of control data is completed, at the image processing unit 14, processing of color image data is started. Processing of the color image data is performed in a manner that element color image data supplied from the image data generating unit (not shown) is processed on the basis of the control data stored in the element color RAM/register units 13-1 to 13-4. Then, color image data thus processed is supplied to the printer unit 15.

20 In the printer unit 15, a printing action is executed on the basis of the processed color image data supplied from the image processing unit 14. While printing actions performed in color copy machines, or the like usually includes the steps of charging, exposure, development, transfer, and fixing; the term "printing action" as used herein refers to an action of transferring an image onto the intermediate transferring member.

During the period when the printing action is being executed, a color code is supplied from the printer unit 15 to the image processing unit 14. The color code is a signal which shows what element color is being currently transferred at the printer unit 15. Through the color code, it is confirmed that a printing action is duly being executed according to the control data. When an element color to be transferred is intended to be changed to the next one, it is necessary to switch the RAM/register unit which supplies control data to the image processing unit 14. The color code is used also for switching the RAM/register unit.

(1-3) Advantage of the first embodiment

As described above, according to the present embodiment, only one image processing unit is provided, so that such a redundancy in constitution that a unused image processing unit exists can be prevented, whereby the production cost can be reduced.

Further, since the configuration is adopted in which the RAM/register units are provided with respect to the respective element color and control data is previously written in; when the element color to be transferred is switched, it is not necessary to change the stored contents of the RAM/register unit; whereby high-speed printing can be performed.

(2) Second embodiment

Next, referring to Fig. 3, a second embodiment of the present invention will be described.

Fig. 3 shows a constitution of the color image forming apparatus according to the second embodiment. In Fig. 3, some of the members constituting the color forming apparatus according to the second embodiment are the same as those of the first embodiment in constitution and operation, so that such members are denoted by the same reference numerals. Specifically, the CPU 11, the chip select signal generating unit 12, the respective RAM/register units 13-1 to 13-4, the image processing unit 14, and the printer unit 15 of the second embodiment are the same as the corresponding members of the first embodiment.

Characteristic features of the second embodiment which are different from those of the first embodiment are as follows:

First, element color additional registers 16-1 to 16-4 are newly provided, in which a signal for causing corresponding RAM/register units 13-1 to 13-4 to be write-enable (Note that, in Fig. 3, Y-color additional register is denoted by REGY, and similarly, M-color additional register is denoted by REGM, C-color additional register is denoted by REGC, K-color additional register is denoted by REGK; and these notations in Fig. 3 are used in the following explanations).

Among these additional registers, the Y-color additional register (REGY) 16-1 is provided so as to form a pair together with the Y-color RAM/register unit

13-1, and connected via the logical sum circuit 17-1 to the chip select signal generating unit 12 and the Y-color RAM/register unit 13-1. That is, these members are connected in the following manner: a chip select signal
5 supplied from the chip select signal generating unit 12, and the signal supplied from the REGY 16-1 are input into the logical sum circuit 17-1; the logical sum circuit 17-1 calculates a logical sum of these signals; and the calculated results are supplied to the Y-color
10 RAM/register unit 13-1.

Thereafter, when it is desired that the Y-color register unit 13-1 is caused to be write-enable regardless of the kind of the chip select signal supplied to the Y-color RAM/register unit 13-1, setting
15 is executed beforehand in a manner that a signal for causing the Y-color RAM/register unit 13-1 to be write-enable is stored in the Y-color additional register (REGY) 16-1. Note that, in this case, the signal to be
20 stored in the REGY 16-1 is defined as a negative logic so as to conform to the configuration in which the chip select signal is defined as a negative logic.

The setting ensures that, even when the chip select signal supplied to the Y-color RAM/register unit 13-1 is not a signal which does not cause the RAM/register unit
25 13-1 to be write enable (for example, MCS-0), the control data, supplied to the Y-color register unit 13-1, is always written in the Y-color RAM/register unit 13-1. This is because when, in the logical sum circuit 17-1, a

logical sum is obtained on the basis of the chip select signal, which does not cause the RAM/register unit 13-1 to be write-enable, and the signal, which is output from the REGY 16-1 and causes the Y-color RAM register unit
5 13-1 to be write-enable, and consequently the Y-color RAM/register unit 13-1 is caused to be write-enable.

In the same manner as the REGY 16-1, the other element color additional registers 16-2 to 16-4 are connected, respectively, to the corresponding element
10 color RAM/register units 13-2 to 13-4 and to the logical sum circuits 17-2 to 17-4. Further, similarly to the case of the REGY 16-1, storing of a signal, which causes the corresponding RAM/register unit 13-2 to 13-4 to be write-enable, can be set regardless of the kind of the
15 chip select signal.

The above configuration ensures that, when the user wishes to write a same control data into his desired plurality of RAM/register units, only by setting the plurality of additional registers in such a manner as to
20 conform to this, writing into the plurality of RAM/register units can be executed at one time. For this reason, it is not necessary to repeat the same writing action with respect to the plurality of RAM/register units, thereby enhancing the efficiency of the writing
25 action.

A second feature is that a dummy color code register 18 (in Fig. 3, denoted as "DCCODEREG") which outputs a dummy color code is newly provided.

Specifically, as shown in Fig. 3, a selector 19 (denoted as "SEL" in Fig. 3) is provided on the signal line which connects the printer unit 15 and the image processing unit 14; whereby one input of the selector 19 is supplied with a color code given from the printer unit 15, the other input of the selector 19 is supplied with a dummy color code supplied from the dummy color code register 18, and the color code and the dummy color code can be switched at the selector 19. The term "dummy color code" as used herein refers to a signal which exhibits the same function as a color code.

Owing to this configuration, when an evaluation is performed as to whether the apparatus according to the present invention operates properly, if the operator desires to cause the image processing unit 14 to read control data of an element color (for example, M-color) different from the element color (Y color) under a transferring action, his or her desired reading operation can be realized only by switching the selector 19 to cause the dummy color code register 18 to output a dummy color code of the different element color (M-color). Alternatively, when an evaluation is performed, even where the printer unit 15 is not connected to the image processing unit 14, when the selector 19 is switched so as to cause the dummy color code register 18 to supply a dummy color code to the image processing unit 14, the evaluation can be performed in the same manner as the case where the printer unit 15 is

connected thereto.

(3) Other embodiments

(3-1) In the above-mentioned embodiments, the configuration is adopted in which each of the four element color RAM/register units is adapted to store the control data with respect to each element color. Owing to this configuration, when there are common data among the data stored in the RAM/register units, the common data are stored in the respective RAM/register units in duplicate manners. Considering such a relationship among data, a configuration may be adopted in which the fifth RAM/register unit which stores such overlapping control data of duplicate manner, and only the control data unique to each element color is stored in the element color RAM/register units.

(3-2) Further, in the above embodiment, RAM/register units are provided with respect to the four element colors: yellow, magenta, cyan, and black, but the number of the RAM/register units and the types of colors treated by the RAM/register units are not restricted thereto.

(3-2-1) For example, a configuration may be adopted in which further RAM/register units are provided with respect to a mixed color of Y and M, a mixed color of M and C, and a mixed color of C and Y, whereby seven RAM/register units are provided in total.

(3-2-2) Alternatively, the RAM/register units may be provided with respect to three primary colors of light,

red (R), green (G) and blue (B) (that is three RAM/register units in total).

(3-2-3) Further, a configuration may be adopted in which two RAM/register units are provided, writing of the control unit and the printing operation are performed alternately. Specifically, the control data with respect to the first element color is written in the first RAM/register unit, and in accordance with this control data, a printing action of the element color is performed. During the period when the printing action is being performed, a control data associated with the second element color is written in the second RAM/register unit. When the printing action associated with the first RAM/register unit is completed, the printing action is performed in accordance with the control data stored in the second RAM/register unit. During the period when the printing action is being executed, a control data associated with, the third element color is written in. Also in the subsequent operations, writing of control data and an execution of a printing action are executed in such an alternate manner that, during the period when the printing action is being executed with respect to one element color, on the other hand, a control data associated with an element color is written in. In this embodiment, all that need to be done is to provide the two RAM/register units, so that a configuration is made simplified, thus reducing the manufacturing costs.

(4) Advantages of the Invention

As described above, in the present invention, since a plurality of RAM/register units are provided with respect to one image processing unit, the constitution is simplified, so that generating an image processing unit which is not used during the transferring action can be avoided, so that the manufacturing cost can be reduced.

Further, control data of each element color is previously stored in each of the element color RAM/register units before starting the image processing action, so that it is not needed to rewrite the contents of the RAM/register unit when the element color is changed, whereby high-speed printing is enabled.